

Contents

<i>Preface to the Sixth Edition</i>	<i>xiii</i>
<i>Preface</i>	<i>xvii</i>
1. DIGITAL PRINCIPLES	1
1.1 Definitions for Digital Signals	2
1.2 Digital Waveforms	4
1.3 Digital Logic	8
1.4 Moving and Storing Digital Information	14
1.5 Digital Operations	18
1.6 Digital Computers	23
1.7 Digital Integrated Circuits	28
1.8 Digital IC Signal Levels	34
<i>Summary</i>	35
<i>Glossary</i>	36
<i>Problems</i>	37
<i>Answers to Self-Tests</i>	39
2. DIGITAL LOGIC	40
2.1 The Basic Gates—NOT, OR, AND	41
2.2 Universal Logic Gates—NOR, NAND	49
2.3 AND-OR-Invert GATES	59
2.4 Positive and Negative Logic	61
2.5 Introduction to HDL	64
<i>Summary</i>	69
<i>Glossary</i>	70
<i>Problems</i>	71
<i>Answers to Self-Tests</i>	75

3.	COMBINATIONAL LOGIC CIRCUITS	76
3.1	Boolean Laws And Theorems	77
3.2	Sum-of-products Method	83
3.3	Truth Table to Karnaugh Map	86
3.4	Pairs, Quads, and Octets	89
3.5	Karnaugh Simplifications	92
3.6	Don't-care Conditions	97
3.7	Product-of-sums Method	100
3.8	Product-of-sums Simplification	102
3.9	Simplification by Quine-McClusky Method	107
3.10	Hazards and Hazard Covers	110
3.11	HDL Implementation Models	113
	<i>Summary</i>	115
	<i>Glossary</i>	116
	<i>Problems</i>	117
	<i>Answers to Self-Tests</i>	120
4.	DATA-PROCESSING CIRCUITS	122
4.1	Multiplexers	123
4.2	Demultiplexers	131
4.3	1-of-16 Decoder	135
4.4	BCD-to-decimal Decoders	138
4.5	Seven-segment Decoders	141
4.6	Encoders	144
4.7	Exclusive-or GATES	146
4.8	Parity Generators and Checkers	149
4.9	Magnitude Comparator	152
4.10	Read-only Memory	154
4.11	Programmable Array Logic	159
4.12	Programmable Logic	162
4.13	Troubleshooting with a Logic Probe	164
4.14	HDL Implementation of Data Processing Circuits	165
	<i>Summary</i>	167
	<i>Glossary</i>	168
	<i>Problems</i>	169
	<i>Answers to Self-Tests</i>	175
5.	NUMBER SYSTEMS AND CODES	176
5.1	Binary Number System	176
5.2	Binary-to-decimal Conversion	178
5.3	Decimal-to-binary Conversion	182
5.4	Octal Numbers	185
5.5	Hexadecimal Numbers	188

5.6	The ASCII Code	196
5.7	The Excess-3 Code	198
5.8	The Gray Code	199
5.9	Troubleshooting with a Logic Pulser	200
	<i>Summary</i>	202
	<i>Glossary</i>	202
	<i>Problems</i>	203
	<i>Answers to Self-Tests</i>	208

6. ARITHMETIC CIRCUIT **209**

6.1	Binary Addition	210
6.2	Binary Subtraction	214
6.3	Unsigned Binary Numbers	216
6.4	Sign-magnitude Numbers	218
6.5	2'S Complement Representation	219
6.6	2'S Complement Arithmetic	224
6.7	Arithmetic Building Blocks	230
6.8	The Adder-subtractor	232
6.9	Fast Adder	235
6.10	Arithmetic Logic Unit	239
6.11	Binary Multiplication and Division	241
6.12	Arithmetic Circuits Using HDL	241
	<i>Summary</i>	243
	<i>Glossary</i>	244
	<i>Problems</i>	244
	<i>Answers to Self-Tests</i>	246

7. CLOCKS AND TIMING CIRCUITS **247**

7.1	Clock Waveforms	248
7.2	TTL Clock	252
7.3	Schmitt Trigger	254
7.4	555 Timer—Astable	256
7.5	555 Timer—Monostable	259
7.6	Monostables with Input Logic	261
7.7	Pulse-Forming Circuits	265
	<i>Summary</i>	268
	<i>Glossary</i>	269
	<i>Problems</i>	270
	<i>Answers to Self-Tests</i>	272

8. FLIP-FLOPS **273**

8.1	RS Flip-flops	274
8.2	Gated Flip-flops	279

- 8.3 Edge-triggered RS Flip-flops 282
- 8.4 Edge-triggered *D* Flip-flops 285
- 8.5 Edge-triggered *JK* Flip-flops 286
- 8.6 Flip-flop Timing 288
- 8.7 *JK* Master-slave Flip-flops 289
- 8.8 Switch Contact Bounce Circuits 291
- 8.9 Various Representations of Flip-flops 293
- 8.10 Analysis of Sequential Circuits 295
- 8.11 Conversion of Flip-flops: A Synthesis Example 299
- 8.12 HDL Implementation of Flip-flop 301

Summary 304

Glossary 304

Problems 305

Answers to Self-Tests 307

9. REGISTERS 309

- 9.1 Types of Registers 310
- 9.2 Serial In–Serial Out 310
- 9.3 Serial In–Parallel Out 315
- 9.4 Parallel In–Serial Out 318
- 9.5 Parallel In–Parallel Out 322
- 9.6 Applications of Shift Registers 326
- 9.7 Register Implementation in HDL 333

Summary 334

Glossary 335

Problems 335

Answers to Self-Tests 336

10. COUNTERS 339

- 10.1 Asynchronous Counters 340
- 10.2 Decoding Gates 345
- 10.3 Synchronous Counters 348
- 10.4 Changing the Counter Modulus 356
- 10.5 Decade Counters 363
- 10.6 Presetable Counters 368
- 10.7 Counter Design as a Synthesis Problem 376
- 10.8 A Digital Clock *C* 381
- 10.9 Counter Design Using HDL 384

Summary 386

Glossary 386

Problems 386

Answers to Self-Tests 388

11. DESIGN OF SEQUENTIAL CIRCUITS	390
11.1 Model Selection	391
11.2 State Transition Diagram	392
11.3 State Synthesis Table	394
11.4 Design Equations and Circuit Diagram	396
11.5 Implementation Using Read Only Memory	399
11.6 Algorithmic State Machine	402
11.7 State Reduction Technique	408
11.8 Analysis of Asynchronous Sequential Circuit	413
11.9 Problems with Asynchronous Sequential Circuits	416
11.10 Design of Asynchronous Sequential Circuit	418
11.11 FSM Implementation in HDL	423
<i>Summary</i>	425
<i>Glossary</i>	425
<i>Problems</i>	426
<i>Answers to Self-Tests</i>	428
12. D/A CONVERSION AND A/D CONVERSION	430
12.1 Variable, Resistor Networks	431
12.2 Binary Ladders	434
12.3 D/A Converters	440
12.4 D/A Accuracy and Resolution	447
12.5 A/D Converter—Simultaneous Conversion	448
12.6 A/D Converter—Counter Method	452
12.7 Continuous A/D Conversion	455
12.8 A/D Techniques	458
12.9 Dual-Slope A/D Conversion	461
12.10 A/D Accuracy and Resolution	466
<i>Summary</i>	467
<i>Glossary</i>	468
<i>Problems</i>	468
<i>Answers to Self-Tests</i>	470
13. MEMORY	471
13.1 Basic Terms and Ideas	472
13.2 Magnetic Memory	474
13.3 Optical Memory	479
13.4 Memory Addressing	482
13.5 ROMs, PROMs, and EPROMs	487
13.6 RAMs	492
13.6 Sequential Programmable Logic Devices	498
<i>Summary</i>	501
<i>Glossary</i>	502

Problems 503
Answers to Self-Tests 505

14. DIGITAL INTEGRATED CIRCUITS **506**

14.1 Switching Circuits 507
14.2 7400 TTL 512
14.3 TTL Parameters 515
14.4 TTL Overview 523
14.5 Open-collector Gates 526
14.6 Three-state TTL Devices 528
14.7 External Drive for TTL Loads 531
14.8 TTL Driving External Loads 534
14.9 74C00 CMOS 535
13.10 CMOS Characteristics 538
14.11 TTL-to-CMOS Interface 541
14.12 CMOS-to-TTL Interface 544
14.13 Current Tracers 546
Summary 547
Glossary 548
Problems 549
Answers to Self-Tests 554

15. APPLICATIONS **556**

15.1 Multiplexing Displays 557
15.2 Frequency Counters 565
15.3 Time Measurement 569
15.4 Using the ADC0804 571
15.5 Microprocessor-compatible A/D Converters 576
15.6 Digital Voltmeters 582
Summary 591
Problems 591
Answers to Self-Tests 592

16. A SIMPLE COMPUTER DESIGN **593**

16.1 Building Blocks 594
16.3 Register Transfer Language 597
16.4 Macro and Micro Operations 600
16.5 Design of Control Unit 602
16.6 Programming Computer 605
Summary 612
Glossary 612
Problems 613
Answers to Self-Tests 614

APPENDICIES

1. Binary-hexadecimal-decimal Equivalents	615
2. 2's Complement Representation	622
3. TTL Devices	626
4. CMOS Devices	630
5. Codes	632
6. BCD Codes	635
7. Overview of IEEE Std. 91-1984 Explanation of Logic Symbols	641
8. Pinout Diagrams	646
9. Answers to Selected Odd-Numbered Problems	651

Index