Preface

PHILOSOPHY AND GOALS

Electronic Circuit Analysis and Design is intended as a core text in electronics for undergraduate electrical and computer engineering students. The purpose of the third edition of the book is to provide a foundation for analyzing and designing both analog and digital electronic circuits.

The majority of electronic circuit design today involves using integrated circuits (ICs). The entire circuit is fabricated on a single piece of semiconductor material. The IC can contain millions of semiconductor devices and other elements, and can perform complex functions. The microprocessor is an example of such a circuit. The ultimate goal of this text is to understand the operation, characteristics, and limitations of the basic circuits that form these integrated circuits.

Initially, discrete transistor circuits are analyzed and designed. The complexity of the circuits studied is then increased. Eventually the reader should be able to analyze and design the basic elements of integrated circuits, such as digital logic gates.

This text is an introduction to the complex subject of electronic circuits. Therefore, more advanced material is not included. Specific technologies, such as gallium arsenide, which is used in special applications, are also not included, although reference may be made to a few specialized applications. Finally, the layout and fabrication of ICs are not covered, since these topics alone can warrant entire texts.

COMPUTER-AIDED ANALYSIS AND DESIGN (PSPICE)

Computer analysis and computer-aided design (CAD) are significant factors in electronics. One of the most prevalent electronic circuit simulation programs is Simulation Program with Integrated Circuit Emphasis (SPICE), developed at the University of California. A version of SPICE tailored for personal computers is PSpice. A comprehensive appendix on the PSpice circuit modeling program is included in this text. Example programs are also given in Appendix B. Instructors may introduce PSpice at any point in the course.

The text emphasizes hand analysis and design. However, in several places in the text, PSpice results are included and are correlated with the hand analysis results. The PSpice capture schematic diagrams are included, as well as the computer simulation results. Specific computer simulation problems are included at the end of most chapters. However, at the instructor's discretion, PSpice can be used for any exercise or problem, to verify the hand analysis.

In some chapters, particularly the chapters on frequency response and feedback, computer analysis is used more heavily. Even in these situations, however, computer analysis is considered only after the

fundamental properties of the circuit have been covered. The computer is a tool that can aid in the analysis

and design of electronic circuits, but is not a substitute for a thorough understanding of the basic concepts of circuit analysis.

DESIGN EMPHASIS

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Design is the heart of engineering. Good design evolves out of considerable experience with analysis. In this text, we point out various characteristics and properties of circuits as we go through the analysis. The objective is to develop an intuition that can be applied to the design process.

Many design examples, design exercise problems, and end-of-chapter design problems are included in this text. The end-of-chapter design problems are designated with a D. Many of these design examples and problems have a set of specifications that lead to a unique solution. Engineering design in its truest sense does not lead to a unique solution. Although the type of design problem given in the text may not be design in its strictest form, the author believes that this is a first step in learning the design process. A separate section, Design Application, found in the end-of-chapter problems, contains open-ended design problems.

PREREQUISITES

This book is intended for junior undergraduates in electrical and computer engineering. The prerequisites for understanding the material include dc analysis and steady-state sinusoidal analysis of electric circuits and the transient analysis of RC circuits. Various network concepts, such as Thevenin's and Norton's theorems, are used extensively. Some background in Laplace transform techniques may also be useful. Prior knowledge of semiconductor device physics is not required.

ORGANIZATION

The book is divided into three parts. Part 1, consisting of the first eight chapters, covers semiconductor materials, the basic diode operation and diode circuits, and basic transistor operations and transistor circuits. Part 2 addresses more advanced analog electronics, such as operational amplifier circuits, biasing techniques used in integrated circuits, and other analog circuits applications. Part 3 covers digital electronics including CMOS integrated circuits. Six appendices are included at the end of the text.

Part 1. Chapter 1 introduces the semiconductor material and pn junction, which leads to the diode circuits and applications given in Chapter 2. Chapter 3 covers the field-effect transistor, with strong emphasis on the metal-oxide-semiconductor FET (MOSFET), and Chapter 4 presents basic FET linear amplifiers. Chapter 5 discusses the bipolar junction transistor, with basic bipolar linear amplifier applications given in Chapter 6.

The chapters covering MOSFETs (3 and 4) and the chapters covering bipolars (5 and 6) are written independently of each other. Instructors, therefore, have the option of discussing MOSFETs before bipolars as given in the text, or discussing bipolars before MOSFETs in the more traditional manner as shown in the following table.

Possible Order of Initial Chapter F Text		Presentation Traditional	
Chapter	Topic	Chapter	Торіс
1	pn Junctions	1	pn Junctions
2	Diode Circuits	2	Diode Circuits
3	MOS Transistors	5	Bipolar Transistors
4	MOSFET Circuits	6	Bipolar Circuits
5	Bipolar Transistors	3	MOS Transistors
6	Bipolar Circuits	4	MOSFET Circuits

phasis in Chapters 3 through 6 was on the analysis and design techniques, so mixing the two transistor types within a given chapter would introduce unnecessary confusion. However, starting with Chapter 7, both MOS-FET circuits and bipolar circuits are discussed within the same chapter. Finally, Chapter 8, covering output stages and power amplifiers, completes Part 1 of the text.

Part 2. Chapters 9 through 15 are included in Part 2, which addresses more advanced analog electron-

The frequency response of transistors and transistor circuits is covered in a separate Chapter 7. The em-

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ics. In this portion of the text, the emphasis is placed on the operational amplifier and on circuits that form the basic building blocks of integrated circuits (ICs). The ideal operational amplifier and ideal op-amp circuits are covered in Chapter 9. Chapter 10 presents constant-current source biasing circuits and introduces the active load, both of which are used extensively in ICs. The differential amplifier, the heart of the op-amp, is discussed in Chapter 11, and feedback is considered in Chapter 12. Chapter 13 presents the analysis and design of various circuits that form operational amplifiers. Nonideal effects in analog ICs are addressed in Chapter

Part 3. Chapters 16 and 17 form Part 3 of the text, and cover the basics of digital electronics. The analysis and design of MOS digital electronics is discussed in Chapter 16. The emphasis in this chapter is on CMOS circuits, which form the basis of most present-day digital circuits. Basic digital logic gate circuits are initially covered, then shift registers, flip-flops, and then basic A/D and D/A converters are presented. Chapter 17 introduces bipolar digital electronics, including emitter-coupled logic and classical transistor-transis-

For those instructors who wish to present digital electronics before analog electronics, Part 3 is written to be independent of Part 2. Therefore, instructors may cover Chapters 1, 2, 3, and then jump to Chapter 16. This jump may be somewhat disconcerting to students, but it is possible.

Appendices. Two appendices are included at the end of the text. Appendix A contains a discussion of PSpice, including examples of various types of analyses. Several examples are presented in which the PSpice circuit schematic diagram is given as well as the output response. This will allow the reader to get started with PSpice.

Answers to selected end-of-chapter problems are given in Appendix B.

tor logic circuits.

14, and applications, such as active filters and oscillators, are covered in Chapter 15.

section.

FEATURES OF THE THIRD EDITION

- A short introduction at the beginning of each chapter links the new chapter to the material presented in
 previous chapters. The objectives of the Chapter, i.e., what the reader should gain from the chapter, are
 presented in the Preview section and are listed in bullet form for easy reference.
- Each major section of a chapter begins with a restatement of the objective for this portion of the chapter.
- An extensive number of worked examples are used throughout the text to reinforce the theoretical concepts being developed. These examples contain all the details of the analysis or design, so the reader does not have to fill in missing steps.
- An Exercise Problem follows each example. The exercise problem is very similar to the worked example so that readers can immediately test their understanding of the material just covered. Answers are given for each exercise problem so readers do not have to search for an answer at the end of the book. These exercise problems will reinforce readers' grasp of the material before they move on to the next
- Test Your Understanding exercise problems are included at the end of most major sections of the chapter. These exercise problems are, in general, more comprehensive that those presented at the end of an example. These problems will also reinforce readers' grasp of the material before they move on to the next section. Answers to these exercise problems are also given.

Problem Solving Techniques are given throughout each chapter to assist the reader in analyzing circuits.

- Although there can be more than one method of solving a problem, these Problem Solving Techniques are intended to help the reader get started in the analysis of a circuit.
 A Design Application is included as the last section of each chapter. A specific electronic design related to that chapter is presented. Over the course of the book, students will learn to build circuits for an electronic design related.
- tronic thermometer. Though not every Design Application deals with the thermometer, each application illustrates how students will use design in the real world.
 A Summary section follows the text of each chapter. This section summarizes the overall results derived in the chapter and reviews the basic concepts developed. The summary section is written in bullet form
- for easy reference.
 A Checkpoint section follows the Summary section. This section states the goals that should have been met and states the abilities the reader should have gained. The Checkpoints will help assess progress be-
- met and states the abilities the reader should have gained. The Checkpoints will help assess progress before moving to the next chapter.

 A list of review questions is included at the end of each chapter. These questions serve as a self-test to
- A list of review questions is included at the end of each chapter. These questions serve as a self-test to help the reader determine how well the concepts developed in the chapter have been mastered.
- A large number of problems are given at the end of each chapter, organized according to the subject of each section. Many new problems have been incorporated into the third edition. Design oriented problems are included as well as problems with varying degrees of difficulty. A "D" indicates design-type problems, and an asterisk (*) indicates more difficult problems. Separate computer simulation problems and open-ended design problems are also included.
- Answers to selected problems are given in Appendix F. Knowing the answer to a problem can aid and reinforce the problem solving ability.
- Manufacturers' data sheets for selected devices and circuits are given in Appendix C. These data sheets should allow the reader to relate the basic concepts and circuit characteristics studied to real circuit characteristics and limitations.

SUPPLEMENTS

The book is supported by a wide variety of supplements both online and in addition to the text. The book's website contains resources for both instructors and students. The student portion of the site contains two new features: laboratory manual and Extra reading material.

- Laboratory manual includes a broad range of experiments involving diodes, transistors and OPAMPs. They are on both analog and digital circuits and designs.
- Extra reading material—includes the following.
- Appendix A- Physical Constants and Conversion Factors
- Appendix B- Selected Manufacturer's Data Sheets
- Appendix C- Standard Resistor and Capacitor Values
- Appendix D- Reading list

A number of useful links also appear on the site.

The secure and convenient instructor portion of the site contains PowerPoints with all figures from the text, and full solutions.

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