

Configuration of Plastic Packages

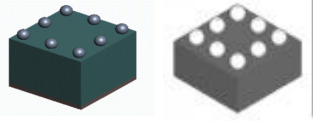

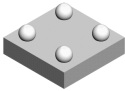


PACKAGING



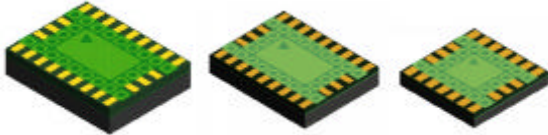
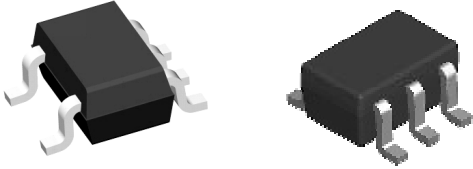
Package
Configuration






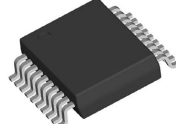
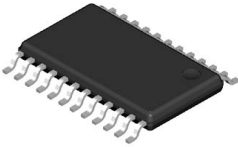
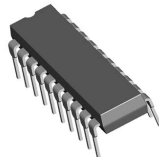
Package Codes

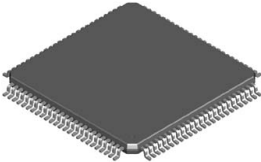
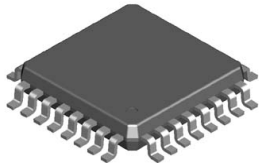
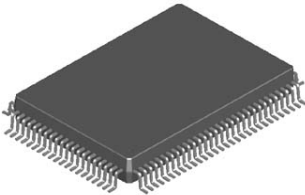
Package
Characteristics


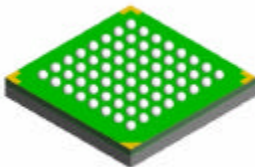
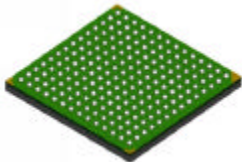
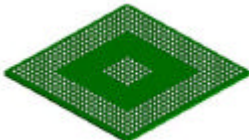


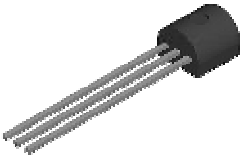
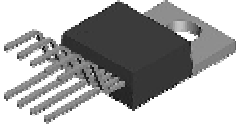

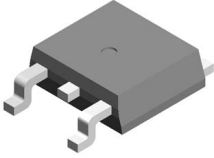
Package Configuration	Package Code	Package Characteristics
<p style="text-align: center;">Micro SMD</p>		<ul style="list-style-type: none"> • Surface Mount Package • Epoxy Coating • No under-fill • Standard pick & place equipment useable • Self-alignment during reflow
<p>Thick Small/large bump</p> 	<p>BP BL</p>	<ul style="list-style-type: none"> • SnPb Eutectic Bump only
<p>Thin Small/large bump</p> 	<p>TP TL</p>	<ul style="list-style-type: none"> • SnPb Eutectic Bump • Pb-free devices available
<p>Ultra thin Small bump</p> 	<p>UL UP</p>	<ul style="list-style-type: none"> • SnPb Eutectic Bump • Pb-free devices available
<p style="text-align: center;">Plastic Small Outline Transistor</p>  <p>SOT23/3Ld SOT23/5Ld SOT23/6Ld</p>	<p>M3 M5 M6 MF</p>	<ul style="list-style-type: none"> • Surface Mount Package • Gull Wing Lead Configuration • Solder Plate Lead Finish • Molded Package • Pb-free devices available
<p style="text-align: center;">SOT223</p>  <p>SOT223/4Ld SOT223/5Ld</p>	<p>MP</p>	<ul style="list-style-type: none"> • Surface Mount Package • Gull Wing Lead Configuration • Solder Plate Lead Finish • Molded Package • Pb-free devices available

Package Configuration	Package Code	Package Characteristics
<p align="center">Leadless Leadframe Package (LLP)</p>		<ul style="list-style-type: none"> • Surface Mount Package • Solder Plate Contact Plating • Low Thermal Resistance • Reduced Board Area
<p align="center">Pullback LLP</p> 	<p align="center">LD LQ</p>	<ul style="list-style-type: none"> • SnPb- and Pb-free devices available
<p align="center">No Pullback LLP</p> 	<p align="center">SD SN SP SQ SR SU</p>	<ul style="list-style-type: none"> • SnPb- and Pb-free devices available
<p align="center">Laminated Chip Scale Package (CSP)</p>  <p align="center">Regular Thin Ultra thin</p>	<p align="center">SLB SLD SLE SL</p>	<ul style="list-style-type: none"> • Surface Mount Package • JEDEC Reg. No. MO-208 • Board area reduction up to 50% compared to standard SOIC or TSSOP • Thermal resistance enhancement accomplished by thermal vias and thermal pads • Pb-free by design
<p align="center">SC-70</p>  <p align="center">SC-70/5Ld SC-70/6Ld</p>	<p align="center">M7 MG</p>	<ul style="list-style-type: none"> • Surface Mount Package • Gull Wing Lead Configuration • Solder Plate Lead Finish • Molded Package • Pb-free devices available

Package Configuration	Package Code	Package Characteristics
<p align="center">MINI-SOIC</p>  <p align="center">8 Leads</p>  <p align="center">10 Leads</p>	MM	<ul style="list-style-type: none"> • Surface Mount Package • Gull Wing Lead Configuration • Solder Plate Lead Finish • Molded Package • Pb-free devices available
<p align="center">Plastic Small Outline Package (SOIC)</p>  <p align="center">Narrow Body</p>  <p align="center">Wide Body</p>	M MA MW WM	<ul style="list-style-type: none"> • Surface Mount Package • Gull Wing Lead Configuration • Solder Plate Lead Finish • Molded Package • EIAJ and JEDEC Package Styles • Footprint Compatible with Ceramic Small Outline Package (SOIC) • Pb-free devices available
<p align="center">Plastic Shrink Small Outline Package (SSOP)</p>  <p align="center">SSOP</p>  <p align="center">SSOP-EIAJ</p>	MS MSA MQA MEA MQ ME	<ul style="list-style-type: none"> • Surface Mount Package • Gull Wing Lead Configuration • Solder Plate Lead Finish • Molded Package • EIAJ and JEDEC Package Styles • Footprint Compatible with Ceramic Small Outline Package (SOIC) • Pb-free devices available
<p align="center">Plastic Thin Shrink Small Outline Packages (TSSOP)</p> 	MXA MXF TM MTC MTD MTE MT MH	<ul style="list-style-type: none"> • Surface Mount Package • Gull Wing Lead Configuration • Solder Plate Lead Finish • Molded Package • EIAJ Package Styles • Pb-free devices available
<p align="center">Molded Dual-In-Line Package (MDIP)</p> 	N NA	<ul style="list-style-type: none"> • Through Hole Package • Solder Plate or Solder Dip Lead Finish • Molded Package • Thermal Enhancements Available • Footprint Compatible with Ceramic Sidebraced Dual-In-Line Package (SB and Cerdip) • Pb-free devices available

Package Configuration	Package Code	Package Characteristics
<p>Thin Quad Flat Pack (TQFP) (t = 1.0 mm)</p> 	<p>V(X,X)</p> <p>VE VJ VQ VS VT VU</p>	<ul style="list-style-type: none"> • Surface Mount Package • Reduced Package Height • Gull Wing Lead Configuration • Solder Plate Lead Finish • Molded Package • Thermal Enhancements Available • High Density Package Application • Pb-free devices available
<p>Low Profile Quad Flat Pack (LQFP) (t = 1.4 mm)</p> 	<p>V(X,X)</p> <p>VB VE VH VJ VN VP VT VV VY</p>	<ul style="list-style-type: none"> • Surface Mount Package • Reduced Package Height • Thermally enhanced with Heat Spreader • Gull Wing Lead Configuration • Solder Plate Lead Finish • Molded Package • High Density Package Application • Pb-free devices available
<p>Plastic Quad Flat Pack (PQFP) (t > 1.4 mm)</p> 	<p>V(X,X)</p> <p>VC VD VE VF VG VH VJ VK VL VM VO VQ VU</p>	<ul style="list-style-type: none"> • Surface Mount Package • Gull Wing Lead Configuration • Solder Plate Lead Finish • Molded Package • Thermal Enhancements Available • High Density Package Application • Pb-free devices available

Package Configuration	Package Code	Package Characteristics
<p>Plastic Leaded Chip Carrier (PLCC)</p> 	<p>V VA</p>	<ul style="list-style-type: none"> • Surface Mount Package • J-Bend Lead Configuration • Solder Plate Lead Finish • Molded Package • Footprint Compatible with Ceramic Quad J-Bend (CQJB) • Thermal Enhancements Available • Pb-free devices available
<p>Fine Pitch Ball Grid Array (FBGA)</p> 	<p>SLC SM</p>	<ul style="list-style-type: none"> • Surface Mount Package • JEDEC Reg. No. 205 • SnPb Eutectic Bump • Epoxy Coating • No under-fill • Standard pick & place equipment useable • Self-alignment during reflow • Pb-free devices on request
<p>Low Profile Ball Grid Array (LBGA)</p> 	<p>UF</p>	<ul style="list-style-type: none"> • Surface Mount Package • Reduced Package Height • SnPb Eutectic Bump • Epoxy Coating • No under-fill • Standard pick & place equipment useable • Self-alignment during reflow • Pb-free devices on request
<p>Plastic Ball Grid Array (PBGA)</p> 	<p>U(X,X) UC</p>	<ul style="list-style-type: none"> • Surface Mount Package • SnPb Solder Balls • No under-fill • Standard pick & place equipment useable • Self-alignment during reflow

Package Configuration	Package Code	Package Characteristics
<p style="text-align: center;">TO Packages</p>		<ul style="list-style-type: none"> • Through Hole and Surface Mount Package • Solder Plate Lead Finish • Molded Package • Designed with Heat Sink for High Power Applications (TO-220)
<div style="display: flex; justify-content: space-around;"> <div style="text-align: center;"> <p>TO-92</p>  </div> <div style="text-align: center;"> <p>TO-220</p>  </div> </div>	<p>TO92: Z ZA</p> <p>TO-220: T TA</p>	<ul style="list-style-type: none"> • Pb-free devices available
<div style="display: flex; justify-content: space-around;"> <div style="text-align: center;"> <p>TO-263</p>  </div> <div style="text-align: center;"> <p>TO-252</p>  </div> </div>	<p>TO-263: S TS</p> <p>TO-252: DT TD</p>	<ul style="list-style-type: none"> • Pb-free devices available